



analog RF "radio frequency" simulation "non-li:

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[PDF] ► A frequency-domain, Volterra series-based behavioral **simulation** tool for **RF** ...

I Vassiliou, A Sangiovanni-Vincentelli - PROCEEDINGS OF THE IEEE ..., 1999 - Citeseer

... design of analog and mixed-signal systems, a top-down, constraint-driven design methodology for **analog** circuits has ... a specific pole-zero representation, that can result in very long **simulation** times. ... a new technique to model and simulate deterministic effects of **RF** systems, based ...

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[PDF] ► ... **simulation** for analysis, design, and optimization of opto-electronic, **radio frequency** ...

FM Rotella - 2000 - www-tcad.stanford.edu

... boundary conditions in a mixed circuit and device **simulation** where device physics play a crucial role in high frequency effects in **radio frequency** devices, radiative recombination ... **RF simulation** is a relatively new field and circuit models are generally derived from **analog** ...

Cited by 12 - Related articles - View as HTML

[PDF] ► Introduction to **RF simulation** and its application

KS Kundert - IEEE Journal of Solid-State Circuits, 1999 - Citeseer

... 1 of 47 Version 2, 23 April 2003 **Radio-frequency (RF)** circuits exhibit several distinguishing characteristics that make them difficult to simulate using traditional Spice ... BJTs have long been used in high-frequency **analog** circuits and their models are well suited for **RF** circuits. ...

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[PDF] ► Efficient per-nonlinearity distortion analysis for **analog** and **RF** circuits

P Li, LT Pileggi - IEEE Transactions on Computer Aided Design of ..., 2003 - Citeseer

... LI AND PILEGGI: EFFICIENT PER-NONLINEARITY DISTORTION ANALYSIS FOR **ANALOG** AND **RF** CIRCUITS ... distortions (which are usually the largest distortion components for an **RF** system ... analysis even though different linearizations might be used in **simulation** iterations. ...

Cited by 8 - Related articles - View as HTML - BL Direct - All 6 versions

[BOOK] Design, **simulation** and applications of inductors and transformers for Si **RF** ICs

AM Niknejad, RG Meyer - 2000 - books.google.com

... **Analog** circuits and signal processing) Includes bibliographical references and index ... use of on-chip inductive elements (inductors and transformers) in silicon (Si) **radio-frequency (RF)** integrated circuits ... in a user-friendly software tool ASITIC (Analysis and **Simulation** of Inductors ...

Cited by 79 - Related articles - BL Direct - All 2 versions

[BOOK] **Radio-frequency** microelectronic circuits for telecommunication applications

YE Papapanos - 1999 - books.google.com

... concentrates on the design and development of integrated circuits operating in the **radio-frequency** range, using ... The most popular type of bipolar transistors that it is used in **RF** IC design is ... In **analog** electronic systems, the bipolar transistors are usually biased to operate in the ...

Cited by 9 - Related articles - All 2 versions

[PDF] ► Computer-aided analysis of nonlinear microwave **analog** circuits using frequency- ...

CR Chang - 1990 - people.engr.ncsu.edu

... **RF** — **Radio frequency**. VCO — Voltage control oscillator. ... In chapter 4, the arithmetic operator method is verified using the **simulation** of microwave amplifier circuits. ... Although the meth- ods

for analyzing nonlinear **analog** circuits are varied, they are all based on solving a ...

Cited by 12 - Related articles - View as HTML - All 2 versions

[Adaptive CMOS predistortion linearizer for fiber-optic links-](#) ► psu.edu [PDF]

R Sadhwani, B Jalali - Journal of Lightwave Technology, 2003 - opticsinfobase.org

... The low-cost linearizer can reduce the cost or improve the performance of **analog** and multilevel optical links. ... In addition, in **radio frequency (RF)** systems, the input signal amplitude is a small signal () quantity. ... Fig. 2. Predistortion system mathematical **simulation** model. (6) (7) ...

Cited by 27 - Related articles - All 14 versions

[PDF] ► ... system combining knowledge-based techniques with optimization and **simulation**

T Ketonen, P Lounamaa, JK Nurminen - National Conference on Artificial ..., 1987 - Citeseer

... of our ideas in a project developing a CAD system called RFT (**Radio Frequency Tools**) for ... techniques have been integrated with **simulation** and optimization techniques in an **analog** CAD system. ... Although our application, **RF**-design, is a highly specialized field we believe that ...

Cited by 3 - Related articles - All 7 versions

[PDF] ► An MOS transistor model for **RF** IC design valid in all regions of operation

C Enz... - IEEE Transactions on Microwave Theory and ..., 2002 - cmsd.cm.nctu.edu.tw

... drain current, but they do not add any poles and are, therefore, invisible for ac **simulation**. ... in turn, moves the operating point of **analog** transistors more and more from strong inversion to ... **RF** MOST are usually biased in forward saturation corresponding to the region below the 45 ...

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